

**REMARKS**

Reconsideration and allowance are respectfully requested. Claims 1, 5, 10-12, 16 and 20 have been amended. Claims 1-23 are pending.

Claims 1-15 and 20-23 stand rejected under 35 U.S.C. 112, second paragraph. Claims 5, 11 and 20 have been amended to remove “electronics assembly engineering system”. Claim 1 has been amended to define a step of employing the computer subsystem in an electronics assembly engineering system. It is submitted that all claims are in full compliance with 35 U.S.C. 112. Therefore, the rejection should be withdrawn.

Claims 1-2, 5-6, 9-13, and 16-23 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Bolnick et al. in view of Choy. The independent claims have been amended to define the invention more clearly and thus, obviate the rejection. In particular, the independent claims as amended recite that a save-as function for the data structures is entirely excluded so as to be incapable of being executed. The Examiner admits that Bolnick et al. includes a save-as function. However, Bolnick et al. does not teach or suggest entirely excluding a save-as function so as to be incapable of being executed as claimed. In fact, Bolnick et al. provides the save-as function to enable a user to save a layout (see column 17, lines 20-28). Therefore the rejection should be withdrawn.

Claims 3-4, 7-8, and 14-15 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Bolnick et al. in view of Choy and further in view of Ferrel et al. These claims are dependent claims and are considered to be allowable for the reasons advanced above, and for the additional reason that the added subject matter thereof is neither taught nor suggested by the prior art of record.

All objections and rejections having been addressed, it is respectfully submitted that the subject application is in condition for allowance and a Notice to that effect is earnestly solicited.

Respectfully submitted,



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